

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Matthew B. Haycock et al.

Serial No.:

09/644463

August 23, 2000

Filed: Title:

Docket No.: 884.303US1 SIMULTANEOUS BIDIRECTIONAL PORT WITH SYNCHRONIZATION

CIRCUIT TO SYNCHRONIZE THE PORT WITH ANOTHER PORT

Assignee:

Intel Corporation

Customer No: 21186

Group Art Unit: 2181

Examiner: Raymond Phan

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

MAR 0 3 2004

Technology Center 2100

In response to the Office Action dated 26 November 2003, the applicants request reconsideration of the above-identified application in view of the following.